## **Objective:

1* Calculate the theoretical \& experimental complex, apparent, real \& reactive power, \& the power factor.
2* Determine the parallel reactance \& component values required for power factor correction.

## **Theory:

A-The complex power:
It can be determined by
1*the general complex power equation:
$\hat{S}=$ Veff $\tilde{I}^{*}$ eff $=$ Veff Ieff $\_(\theta v-\theta i)=S \angle \theta=P+j Q$
Where I \& V are RMS values.
$2^{*}$ the sum of individual component powers (also I,V in RMS)
\#For R:

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Pr= Veff Ieff= V^2reff/R= I^2reff*R
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Qr=0
\#For C:
Pc=0
Qc=Vceff*Iceff=V^2ceff/Xc= I^2ceff*Xc
\#For L:
$\mathrm{Pl}=0$

$\hat{S}=\hat{\tilde{V}} \mathrm{e} f \mathrm{II}^{*}$ eff $f=\Sigma P+j[Q L-Q c]$
Where $P$ is real or average power $\& Q$ is the reactive power.
B- the apparent power \& the power factor:
it's can be obtain by using the following formulas: apparent power (V.A) Veff*Ieff= ( $\left.P^{\wedge} 2+Q^{\wedge} 2\right)^{\wedge} 1 / 2$ $P F=$ average power/apparent power= $P /($ Veff*Ieff $)=\cos$ ( $\theta \mathrm{v}-\theta \mathrm{i}$ )

## C- power factor correction:

it's achieved by limiting the reactive power in the circuit, this lead to make the value of the apparent power closer to
the value of the average power (it's aim to make the power factor as possible equals to 1.
The basic objective of the power factor correction is to decrease the power cost (the reactive power \& it's cost are not desired).
To find the right load should selected to make the PF $=1$ follow the steps:
$1^{*}$ if the circuit has inductive load then the capacitor load should added to make $P F=1$ (correction load)
\& it's value determine by: $X c=V^{\wedge} 2 / Q L$
Where QL is the reactive power of the inductor load in the circuit.
Then L=XL/W
$2^{*}$ if the circuit has capacitor load then the inductor load should added to make $P F=1$ (correction load)
\& it's value determine by: XL=V^2/Qc
Where Qc is the reactive power of the capacitor load in the circuit.
Then $C=1 /\left(w^{*} \times c\right)$

## ** Equipment:

DMM , FG, Osicll(CRO) ,resistor, leads, capacitive , inductive \& resistance boxes, breadboard, wires.

## **Procedure:

## Part A:

1- use the FG to generate a sinusoidal wave of 2 Vpk \& 1000 Hz (as we learn in previous experiment).
2- Provide $2.2 \eta$ F, $100 \Omega$ by using the suitable boxes \& then select the desired values.
3- Fix the component as shown in the figure:


4- determine the value of VR1 by connected channel one to it(read the value peak - peak)
5- Divide the above by two to take the value in peak.
6- Divide the above by square root of two to take the value in RMS.
7- Measured the value of the phase angle by connect channel two between the capacitor \& the ground but remain channel one in R1 the press dull \& determine the value of $\theta$.
8- Calculate the value of PF by take cos of the angle.
9- Calculate PR, QR, Ssource, PL, QL
Note:

## QR ,PL will be zero.

## Part B:

1- calculate the value of $Q L$ then determine the value of the correction load as shown in the calculation part.
2- Calculate the ratio of $L / C$.
3- fix the circuit in the breadboard.
4- calculate the value of VR2 p-p then p-only then in RMS
5 - determine the value of the phase of the circuit by connected channel one between the capacitor \& the ground , \& channel two to R2 \& calculate the value of the phase.
6- take cos of the previous angle to determine the PF note:
$1^{*}$ the value of the phase should be zero(this require to make $P F=1$ ) \& if it doesn' $\dagger$ like this your calculation will be wrong.
2* you added inductor load to generate a reactive power that cancels the one caused by capacitive load.

## **Conclusion:

A. if the circuit contain inductor load the correcting load must be capacitive load connected in parallel with it ,\& if it contain capacitor load the correcting load must be inductive connect in parallel.
B. the average power "P" for inductors \& capacitors equal zero but for the resistance it's not.
C. reactive power " $Q$ "for resistor equal zero but for the capacitors \& inductors it's not.
D. Power factor correction aims to cancel the imaginary part of the equivalent impedance so that the reactive power will be zero \& the real part (resistance) only will be remain.


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Experiment "8" (Complex Power\& Power Factor)
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